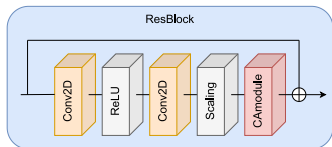
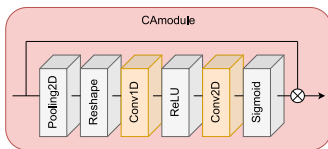


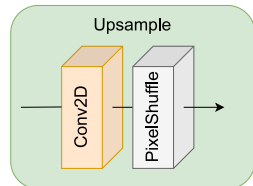
(a) The complete model architecture



(b) The residual block



(c) The channel attention module in the residual block.



(d) The upsampling block.