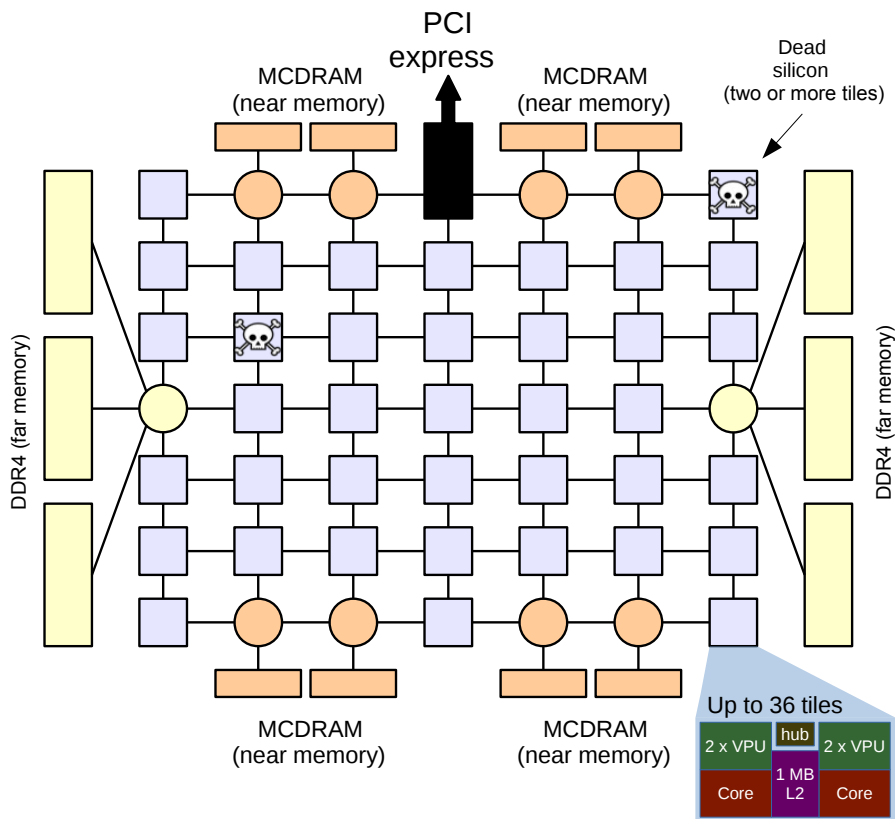


(a) Schematic of a single socket Intel XEON 20 core Broadwell processor (typically two of these to a CPU node, mixed nodes may have one of these and an accelerator in another socket).



(b) Schematic of Intel MIC Knights Landing processor node with 38 tiles (36 usable), each containing two cores with two vector processing units and sharing a 1 MB L2 cache.